

Compliant Control Architecture - Tasks #1772
Create unambiguous converter map for cca state ports

02/14/2014 05:51 PM - Anonymous

Status:	New	Start date:	02/14/2014
Priority:	High	Due date:	
Assignee:		% Done:	0%
Category:		Estimated time:	0.00 hour
Target version:	cca0.5		
Description			
See https://projects.cor-lab.org/projects/flexirob/repository/entry/modules/StateMachines/src/sclib/DemoParticipantCollection.h			