

Compliant Control Architecture - Issues

| # | Tracker | Status | Priority | Subject | Assignee | Updated | Category | % Done |
|------|---------|----------|----------|--|----------|---------------------|----------|--------|
| 290 | Tasks | Feedback | High | Timing for Control | | 08/12/2013 02:21 PM | | 0 |
| 300 | Tasks | New | Normal | TimedContainer / BeatContainer | | 05/17/2011 08:39 PM | | 0 |
| 1332 | Tasks | New | Normal | Rename class BaseComponent | | 01/15/2013 04:36 PM | | 0 |
| 1625 | Tasks | New | Normal | Check conceptual compatibility with DARC | | 09/12/2013 02:59 PM | | 0 |
| 1635 | Tasks | New | Normal | Detect and warn about too slow processing | | 09/23/2013 05:47 PM | | 0 |
| 1772 | Tasks | New | High | Create unambiguous converter map for cca state ports | | 02/14/2014 05:51 PM | | 0 |
| 2049 | Tasks | New | High | Document new beat behavior | | 10/08/2014 06:28 PM | | 0 |
| 2325 | Tasks | New | High | Refactor component lifecycle | | 06/15/2015 07:57 PM | | 0 |